



PATENT APPLICATION

042390.P10047

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Li et al.

Serial No.: 09/897,174

Filed: June 29, 2001

October 24, 2002

Group Art Unit: 2826

Examiner: T. Dickey

#8
Amend
a
3/27/03
avg

For: **LOW-VOLTAGE AND INTERFACE DAMAGE-FREE POLYMER MEMORY DEVICE**

I HEREBY CERTIFY THAT THIS CORRESPONDENCE IS BEING DEPOSITED WITH THE UNITED STATES POSTAL SERVICE IN ACCORDANCE WITH 37 CFR §1.10 AS EXPRESS MAIL IN AN ENVELOPE ADDRESSED TO:

HONORABLE DIRECTOR OF THE UNITED STATES PATENT AND TRADEMARK OFFICE,

WASHINGTON, D.C. 20231, ON: 11.4.02
Date of Deposit

INTEL CORPORATION
Name of Assignee

[Signature] 11.4.02
SIGNATURE DATE

RESPONSE TO RESTRICTION REQUIREMENT

HONORABLE DIRECTOR OF THE UNITED STATES PATENT AND TRADEMARK OFFICE,

Washington, D.C. 20231

SIR:

Applicants believe the following is a complete response to the Office action mailed September 5, 2002. Please consider the following comments and amendments.

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MAR 13 2003
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NOV 19 2002
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